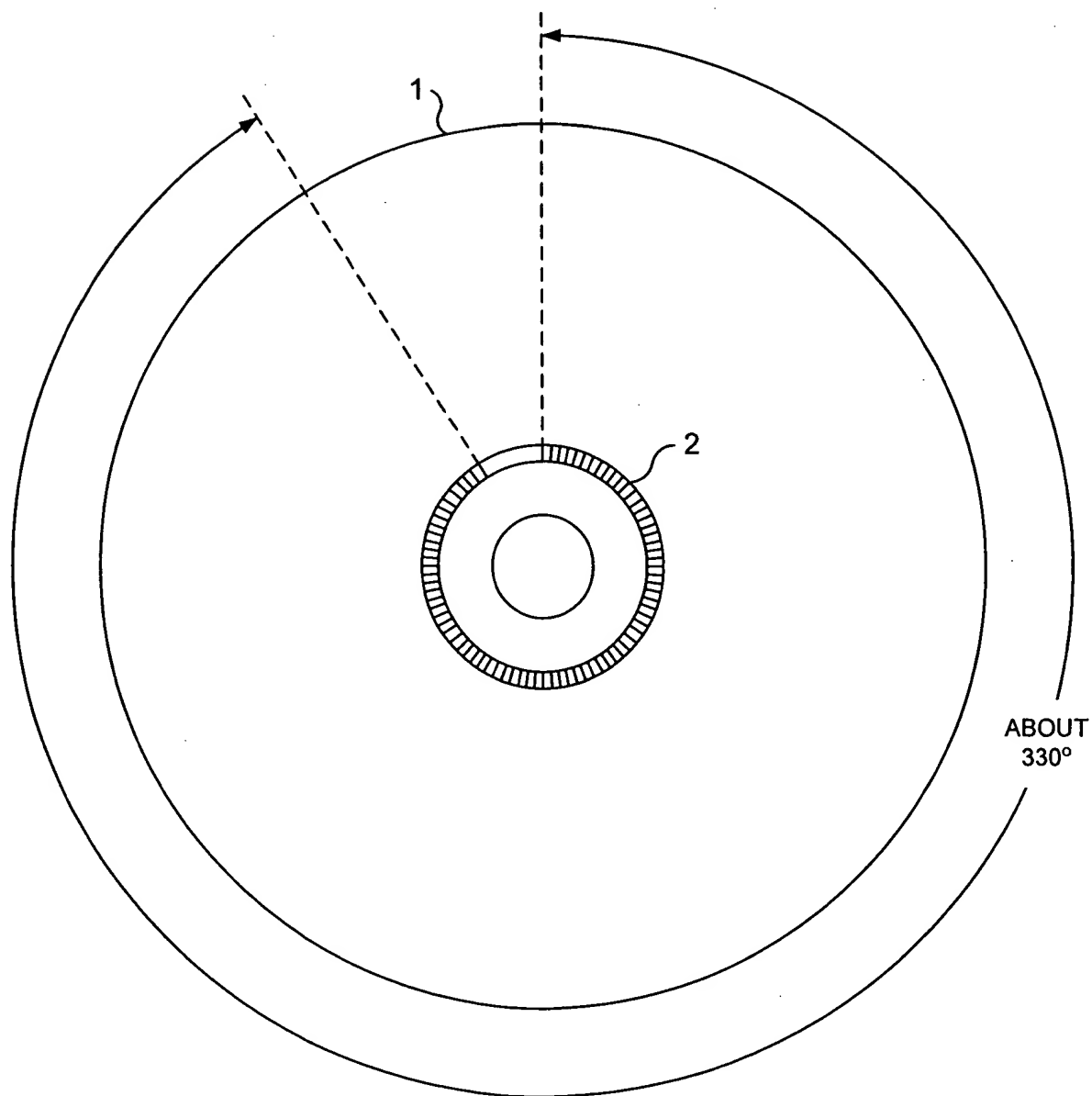




# FIG. 1



(PRIOR ART)



## FIG. 2

5 bytes					
1 byte	4 bytes				
SB <sub>BCA</sub>	BCA – Preamble (All 00h)				1 row
RS <sub>BCA1</sub>	I <sub>0</sub>	I <sub>1</sub>	I <sub>2</sub>	I <sub>3</sub>	4n rows (1≤n≤12)
RS <sub>BCA1</sub>	I <sub>4</sub>	I <sub>5</sub>	I <sub>6</sub>	I <sub>7</sub>	
RS <sub>BCA1</sub>	:	:	:	:	
RS <sub>BCA1</sub>	:	:	:	:	
RS <sub>BCA2</sub>	:	:	:	:	
:					
:					
RS <sub>BCAi-i</sub>					
RS <sub>BCAi</sub>					
RS <sub>BCAi</sub>	Information				
RS <sub>BCAi</sub>					
RS <sub>BCAi</sub>					
RS <sub>BCAi+i</sub>					
:					
:					
RS <sub>BCAn-i</sub>					
RS <sub>BCAn</sub>	:	:	:	:	
RS <sub>BCAn</sub>	:	:	:	:	
RS <sub>BCAn</sub>	I <sub>16n-8</sub>	I <sub>16n-7</sub>	I <sub>16n-6</sub>	I <sub>16n-5</sub>	
RS <sub>BCAn</sub>	EDC <sub>BCA</sub> (4 bytes)				
RS <sub>BCA13</sub>	C <sub>0.0</sub>	C <sub>1.0</sub>	C <sub>2.0</sub>	C <sub>3.0</sub>	4 rows
RS <sub>BCA13</sub>	:	:	ECC <sub>BCA</sub>	:	
RS <sub>BCA13</sub>	:	:	:	:	
RS <sub>BCA13</sub>	C <sub>0.3</sub>	C <sub>1.3</sub>	C <sub>2.3</sub>	C <sub>3.3</sub>	
RS <sub>BCA14</sub>	BCA – Postamble (All 55h)				1 row
RS <sub>BCA15</sub>					

(PRIOR ART)